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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/763,877	02/28/2001	Andrew Augustine Wajs	05683.P028	3487

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EXAMINER

KIM, AHSHIK

ART UNIT

PAPER NUMBER

2876

DATE MAILED: 01/09/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/763,877

Applicant(s)

WAJS, ANDREW AUGUSTINE

Examiner

Ahshik Kim

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2876

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 10/22/02 (RCE).
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_ 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Continued Examination Under 37 CFR 1.114*

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in  
5 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is  
eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e)  
has been timely paid, the finality of the previous Office action (paper #11) has been withdrawn  
pursuant to 37 CFR 1.114. Applicant's submission filed on October 22, 2002 has been entered.

### *Amendment*

10  
2. Receipt is acknowledged of the amendment filed on October 22, 2002. Claims 1-10  
remain for examination.

### *Claim Rejections - 35 USC § 102*

15 The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the  
basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

20 (e) the invention was described in a patent granted on an application for patent by another  
filed in the United States before the invention thereof by the applicant for patent, or on an  
international application by another who has fulfilled the requirements of paragraphs (1),  
(2), and (4) of section 371(c) of this title before the invention thereof by the applicant for  
patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999  
25 (AIPA) do not apply to the examination of this application as the application being examined

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was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5     3.     Claims 1-4 and 6-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Hazama (US 6,089,460).

Re claims 1-4, Hazama teaches a security system comprising an IC card 1 with EEPROM 1b, FPGA (Field Programmable Gate Array) 20 utilizing SRAM (Static Random Access Memory) (col.3, lines 33 – 51). When identification information is etched in EEPROM or non-  
10 volatile memory (col. 1, lines 37+), the IC card contains unique layout of the chip. As further shown in figures 1-7, ciphered data D in EEPROM 1b and mask 1a is outputted to CPU 3 (col. 4, lines 41+), suggesting that distinct cards perform the same tasks such as receiving data from the host, and deciphering information in accordance with unique identification data.

Re claims 6-8, Interacting with the external device 100, IC card 1 cipher/decipher  
15 security information (col. 1, lines 28 – 36; col. 10, line 64 – col. 11, line 2). Hazama further teaches that identification is etched in non-volatile memory (col. 1, lines 37 – 41).

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all  
20 obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary

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skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

- 5     4.     Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Hazama (US 6,089,460) in view of Tanaka (US 4,924,075). The teachings of Hazama have been discussed above.

Hazama fails to specifically teach or fairly suggest that FPGA in IC card is stored in battery-powered RAM.

- 10     Tanaka teaches a smart card 1 powered by internal battery 6 (col. 1, line 67 – col.2, line 10).

In view of Tanaka's teaching, it would have been obvious to an ordinary skill in the art at the time the invention was made to further incorporate a well-known internal battery to the teachings of Hazama in order to make the card versatile. By powering the card with internal  
15     battery, the card can be used with external device, which does not carry power supply. Moreover, significant data can be stored in the memory section being powered by the internal battery. Accordingly, such modification would have been an obvious extension as taught by Tanaka to provide self-supplying power source to internal memory, and therefore an obvious expedient.

- 20     5.     Claims 9 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hazama (US 6,089,460) in view of Cantone et al. (US 5,594,657). The teachings of Hazama have been discussed above.

Hazama fails to specifically teach or fairly suggest of synthesizing, layout and use of  
25     high-level language as a method of programming FPGA.

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Cantone teaches that FPGA can be programmed utilizing synthesis (col. 1, line 62 – col. 2, line 7) and layout tool (col. 16, lines 7 – 18). Cantone further teaches of programming FPGA with user-friendly graphics interface (high level language), and the program is compiled to produce efficient executable (Abstract).

5 In view of Tanaka's teaching, it would have been obvious to an ordinary skill in the art at the time the invention was made to employ a well-known method of FPGA programming to the teachings of Hazama in order to make the programming easier to the users. Furthermore, by compiling/optimizing the source code, the executable program is compact in size requiring less memory space to load the programs. Optimized executable program tend to run faster, and the  
10 executable code can be ported to other FPGA, and thus an obvious expedient.

### Conclusion

I. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure: Chen et al. (US 5,694,471); Deutschmann (US 6,478,230); Hazama (US 6,260,172);  
15 Bialick et al. (US 6,088,802) disclose security systems and smart card including FPGA circuitry.

II. Any inquiry concerning this communication or earlier communications from the examiner should be directed to *Ahshik Kim* whose telephone number is (703)305-5203 . The examiner can normally be reached between the hours of 6:00AM to 3:00PM Monday thru Friday.

20 If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael G. Lee, can be reached on (703) 305-3503. The fax number directly to the Examiner is (703) 746-4782. The fax phone number for this Group is (703)308-7722, (703)308-7724, or (703)308-7382.

25 Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [ahshik.kim@uspto.gov].

*All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly  
30 signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly*

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*set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.*

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.

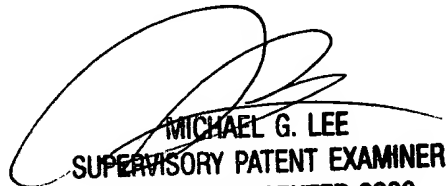
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Ahshik Kim  
Patent Examiner

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January 3, 2003



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